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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Atty. Docket No. (Opt.)

VIEO1100

Applicant
Clarkson, et al.Application Number
09/882,722Filed
June 15, 2001For
Printed Circuit Board Having a Microelectronic
Semiconductor Device Mount Area for Trace
Routing TherethroughGroup Art Unit
2827Examiner
Cuneo, KamandCertification Under 37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on July 30, 2002.

Carolyn J. Williams

Honorable Commissioner
for Patents
Washington, D.C. 20231

VIEO, Inc., 100% owner of the above-identified patent application, as evidenced by the attached Assignment filed on June 15, 2001 (011920/078) with the above-referenced application, hereby revokes all previous Powers of Attorney and appoints all attorneys under Customer No. 25094, all of the firm of Gray Cary Ware & Freidenrich, LLP, to prosecute the above-identified Patent and to transact all business in the Patent and Trademark Office connected therewith, including:

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Respectfully submitted,

VIEO, INC.

By:
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Dated: 30 July, 2002